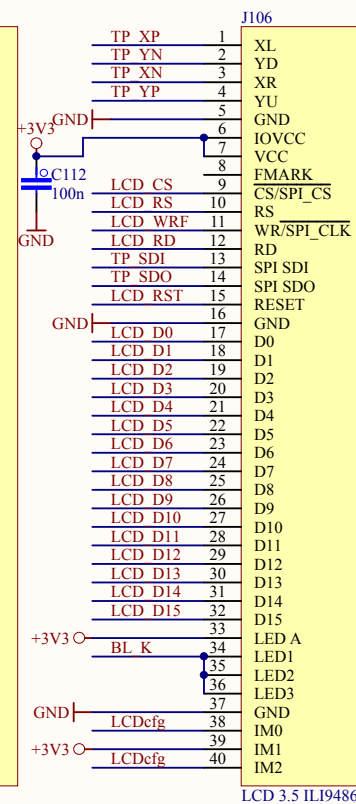
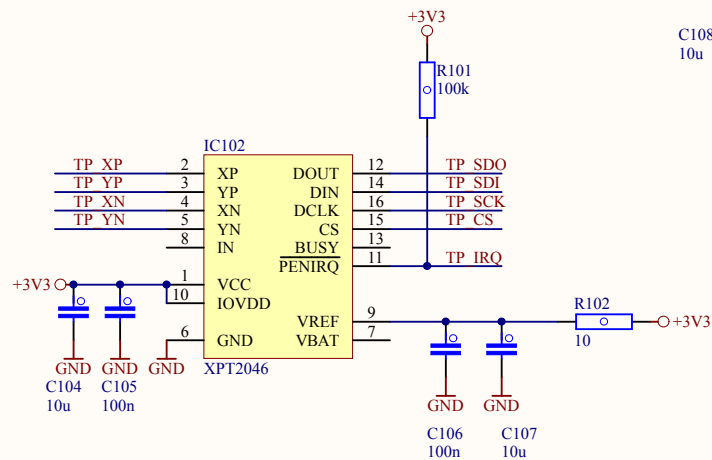
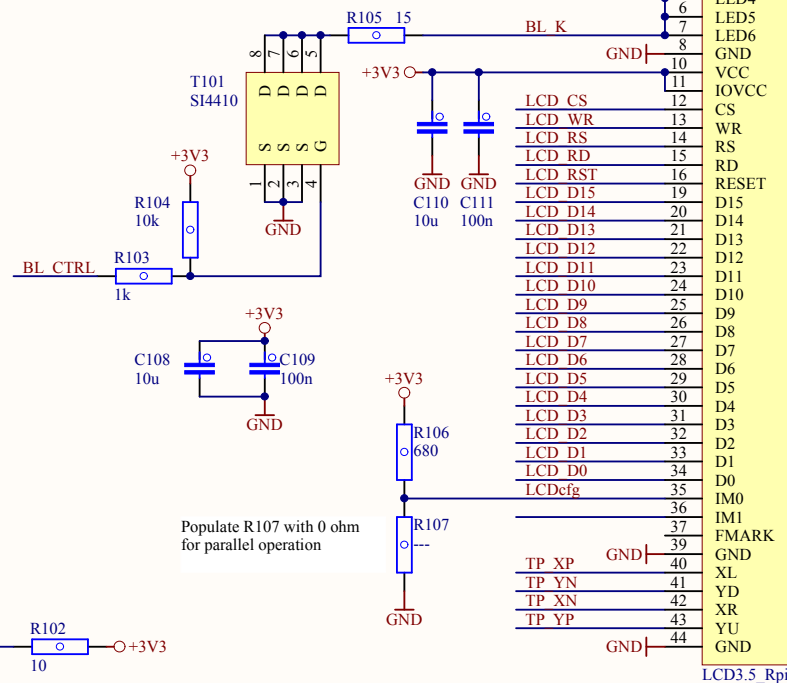
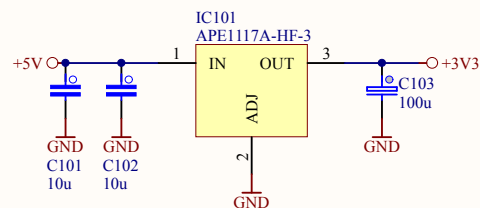
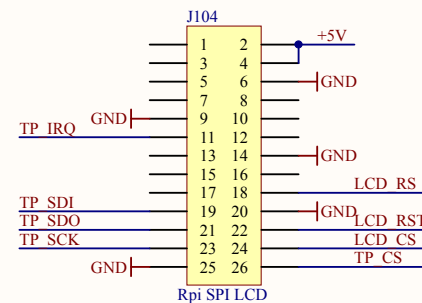
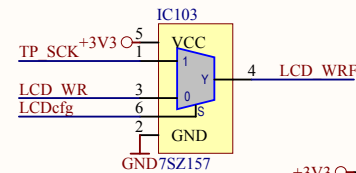
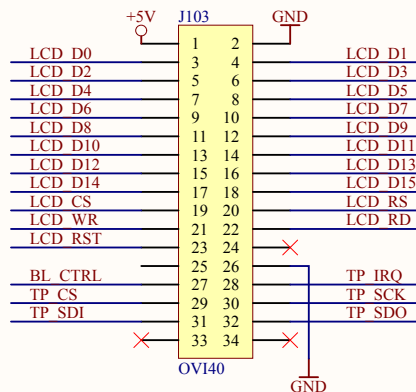
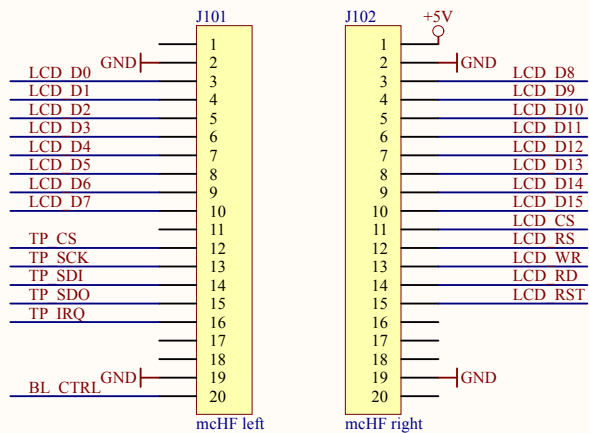




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UI board connectors

J101 (mCHf left)

1	
2	GND
3	LCD D0
4	LCD D1
5	LCD D2
6	LCD D3
7	LCD D4
8	LCD D5
9	LCD D6
10	LCD D7
11	
12	TP CS
13	TP SCK
14	TP SDI
15	TP SDO
16	TP IRQ
17	
18	
19	GND
20	BL_CTRL

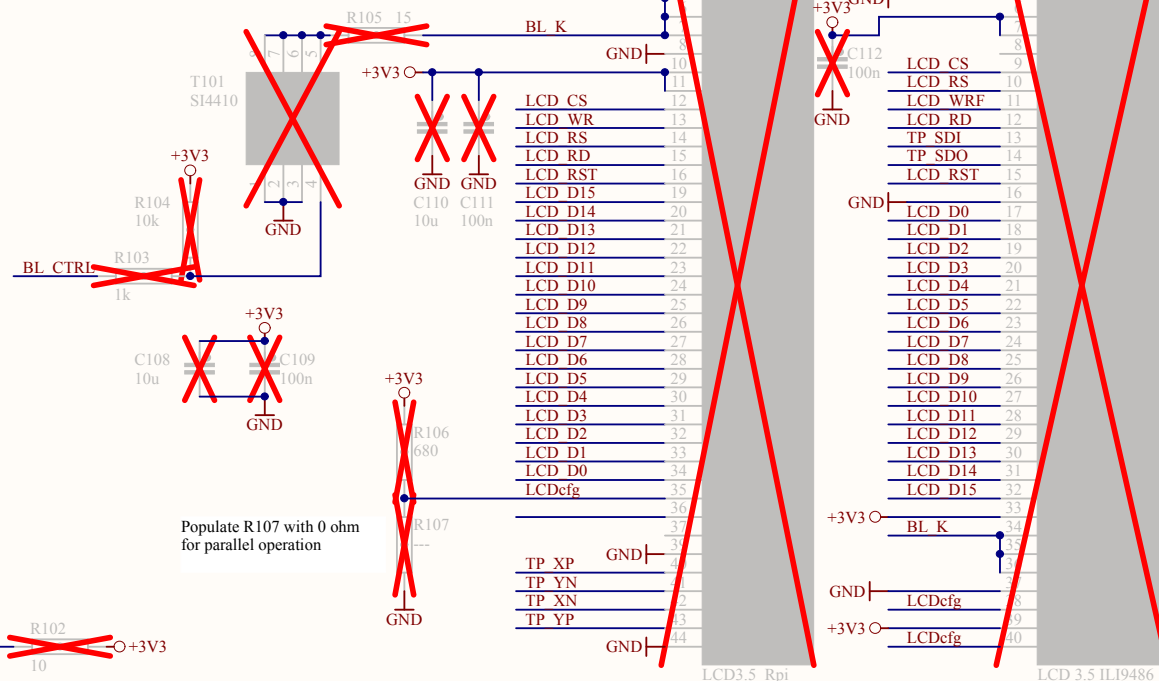
J102 (mCHf right)

1	+5V
2	GND
3	LCD D8
4	LCD D9
5	LCD D10
6	LCD D11
7	LCD D12
8	LCD D13
9	LCD D14
10	LCD D15
11	LCD CS
12	LCD RS
13	LCD WR
14	LCD RD
15	LCD RST
16	
17	
18	
19	GND
20	

J103 (OV140)

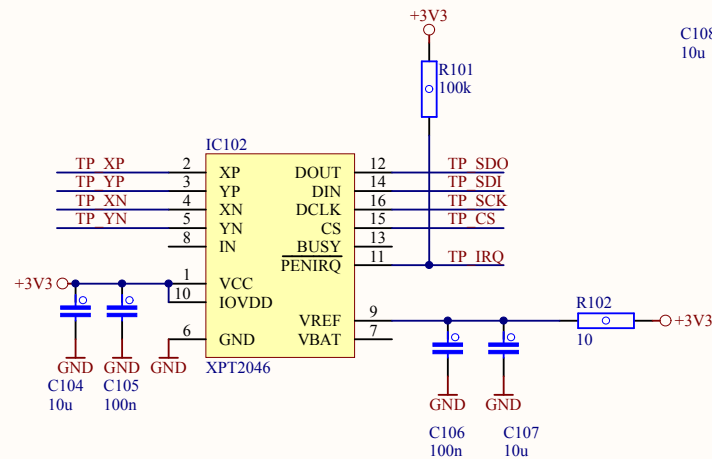
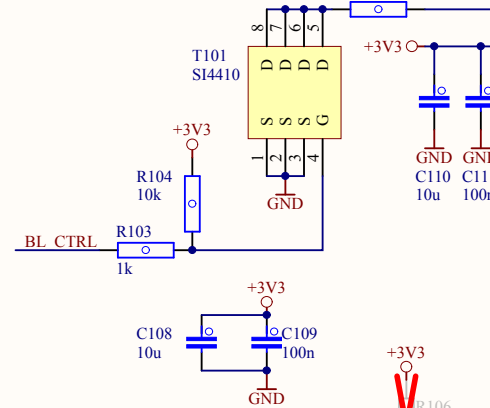
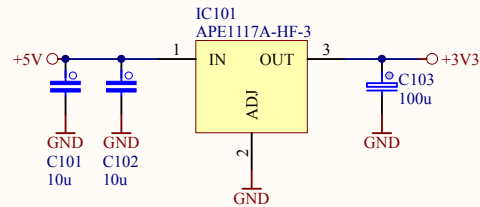
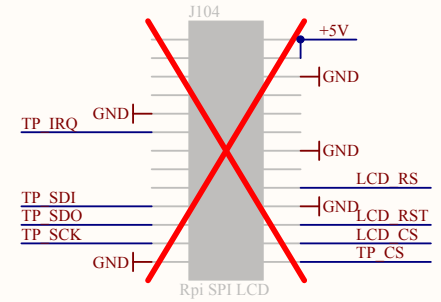
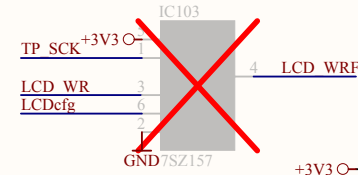
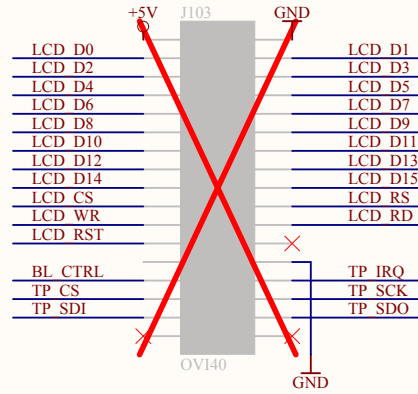
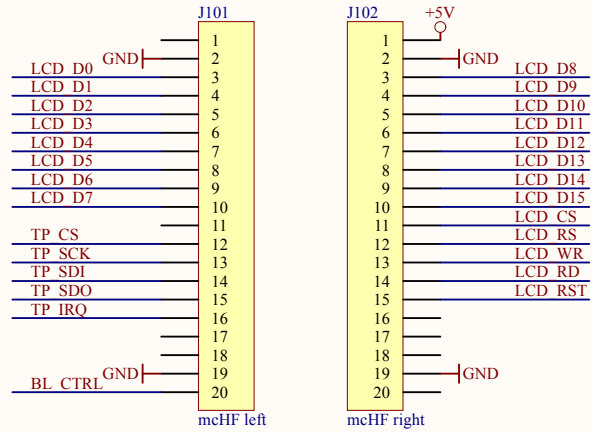
1	+5V
2	
3	LCD D0
4	LCD D2
5	LCD D4
6	LCD D6
7	LCD D8
8	LCD D10
9	LCD D12
10	LCD D14
11	LCD CS
12	LCD RS
13	LCD WR
14	LCD RST
15	
16	BL_CTRL
17	TP CS
18	TP SDI
19	
20	GND

Red 'X' marks indicate that the OV140 connector is not used in this configuration.

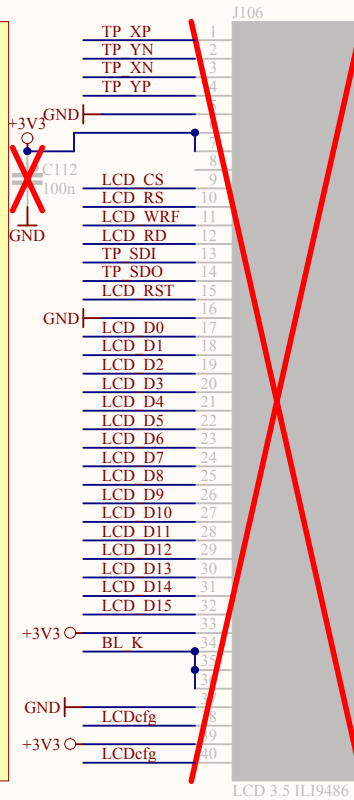
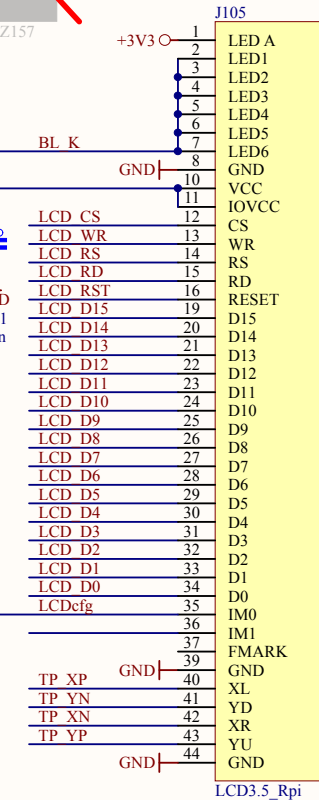
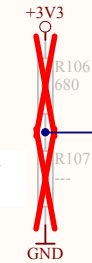


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UI board connectors



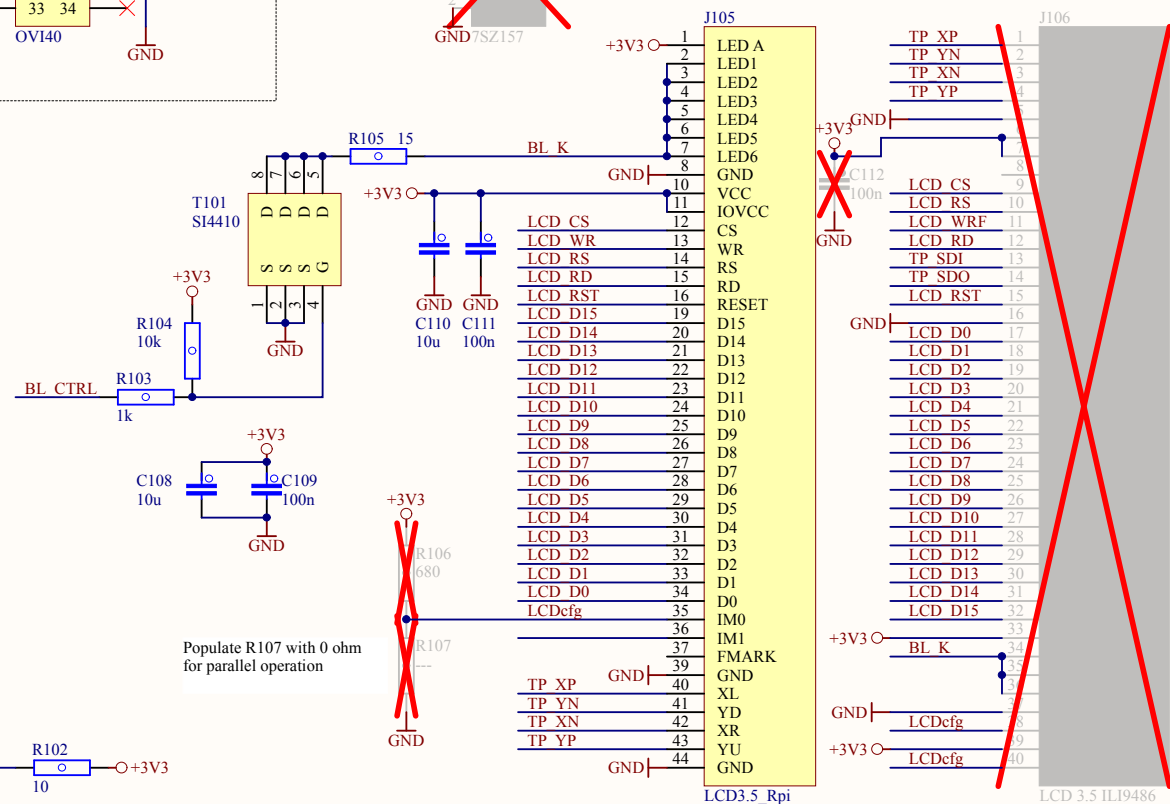
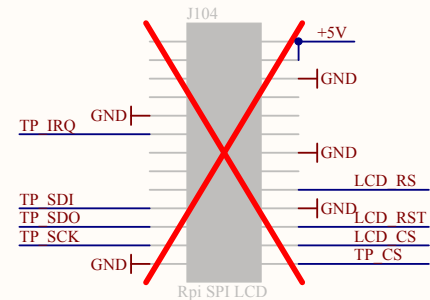
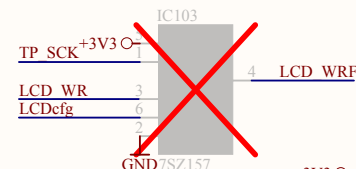
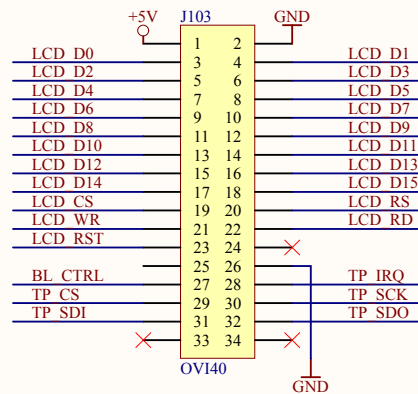
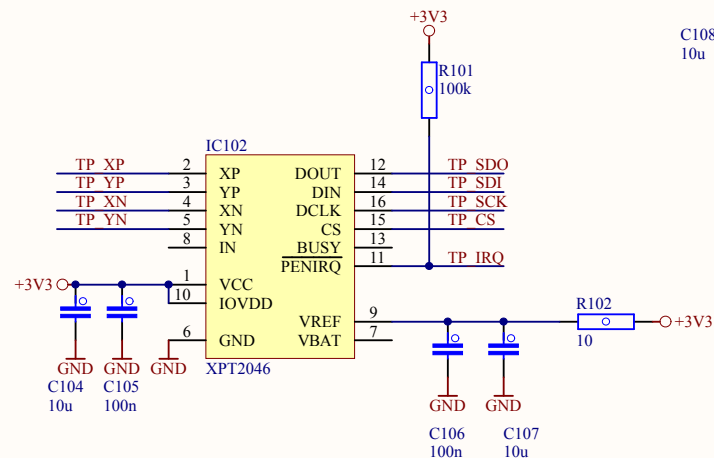
Populate R107 with 0 ohm for parallel operation



Title		
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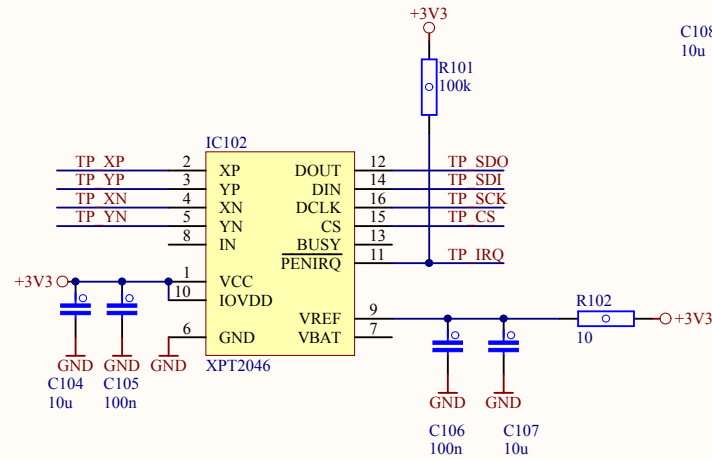
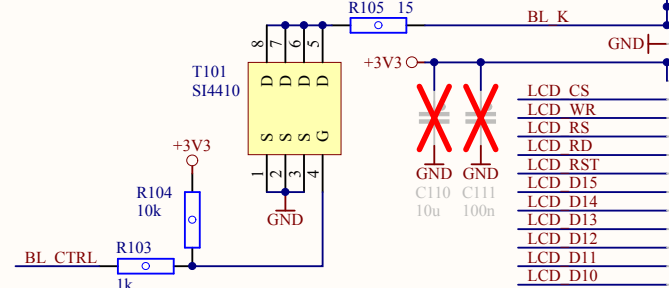
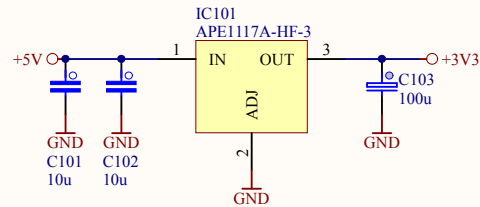
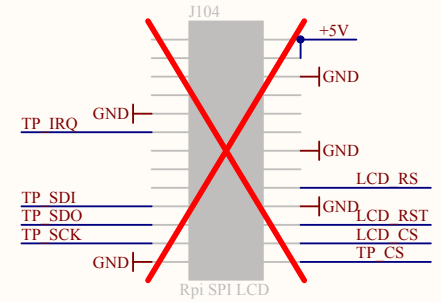
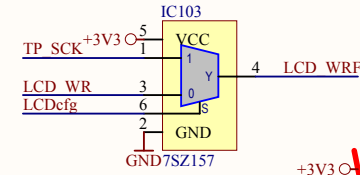
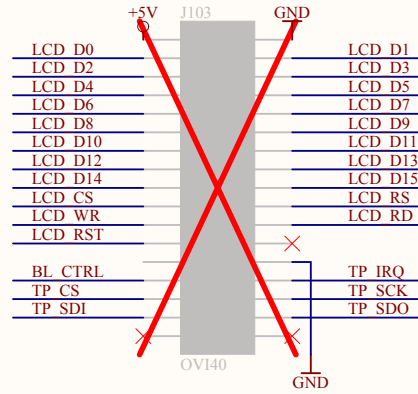
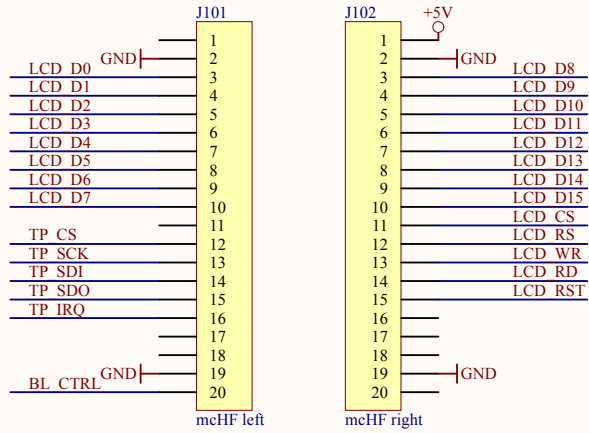
The diagram shows three connectors on the UI board:

- J101 (mcHF left):** A 28-pin connector. Pins 1-7 are LCD D0-D7. Pins 8-14 are TP CS, TP SCK, TP SDI, TP SDO, and TP IRQ. Pins 15-28 are GND.
- J102 (mcHF right):** A 28-pin connector. Pins 1-7 are LCD D8-D15. Pins 8-14 are LCD CS, LCD RS, LCD WR, LCD RD, and LCD RST. Pins 15-28 are GND.
- J103 (OV140):** A 34-pin connector. Pins 1-14 are LCD D0-D15. Pins 15-24 are LCD CS, LCD RS, LCD WR, LCD RST, BL_CTRL, TP CS, TP SDI, and TP SDO. Pins 25-34 are TP IRQ, TP SCK, TP SDO, and GND.

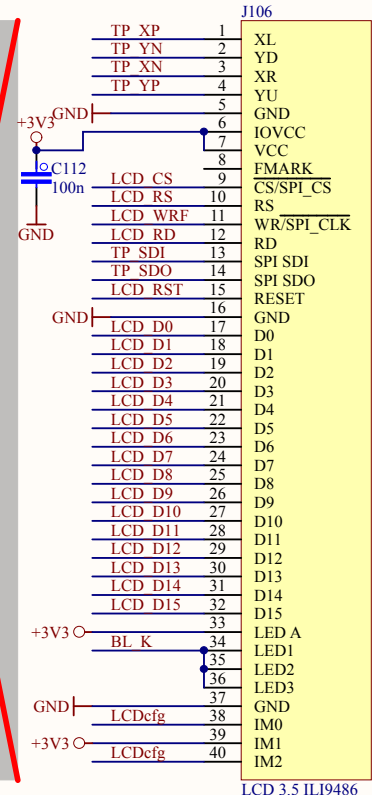
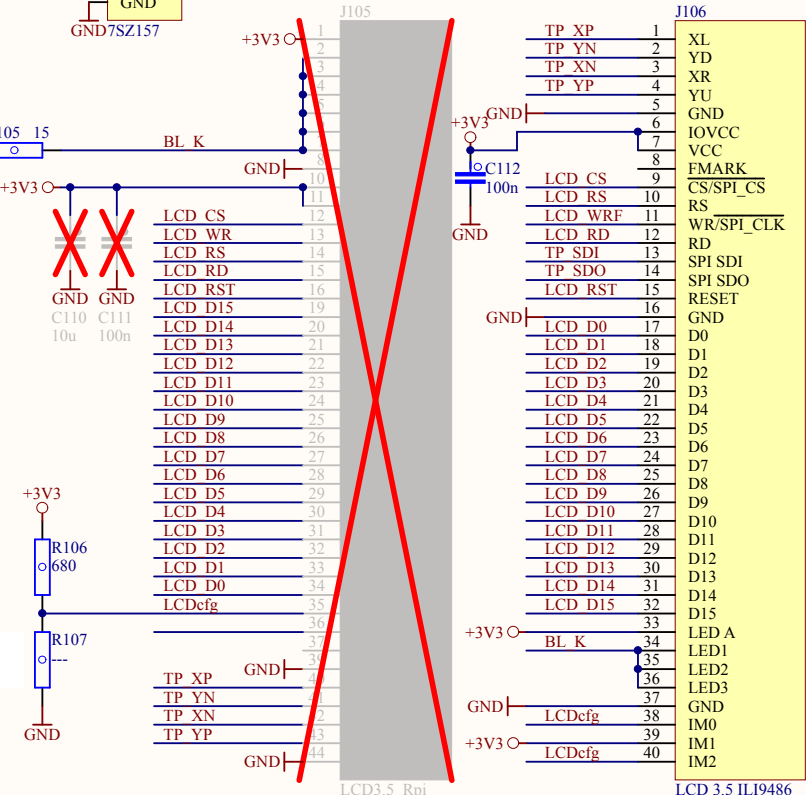


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UI board connectors

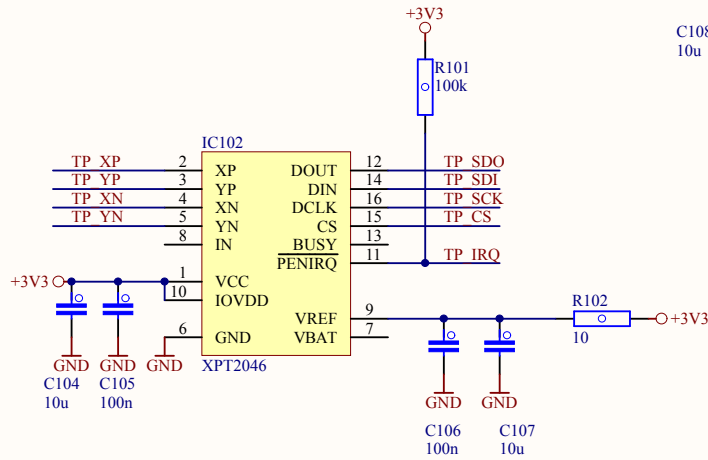
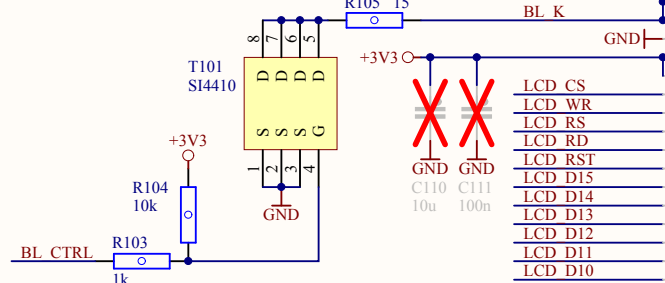
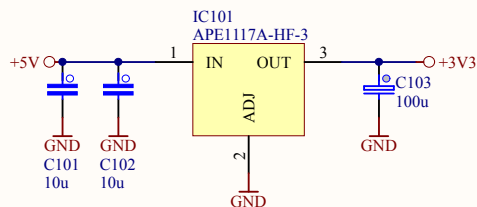
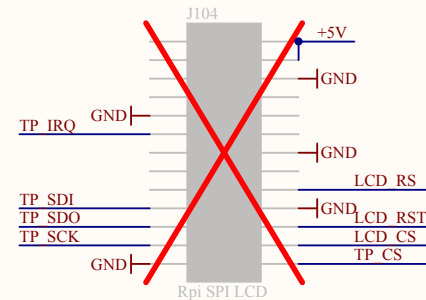
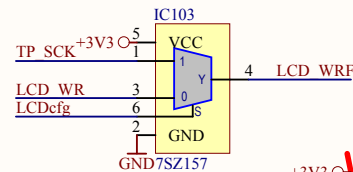
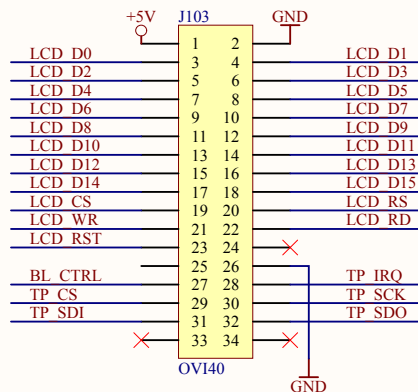
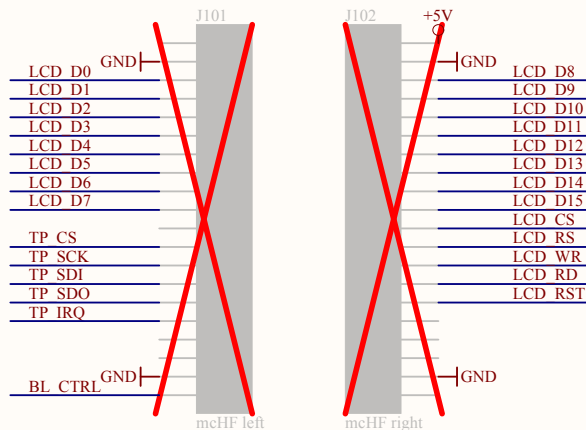


Populate R107 with 0 ohm for parallel operation

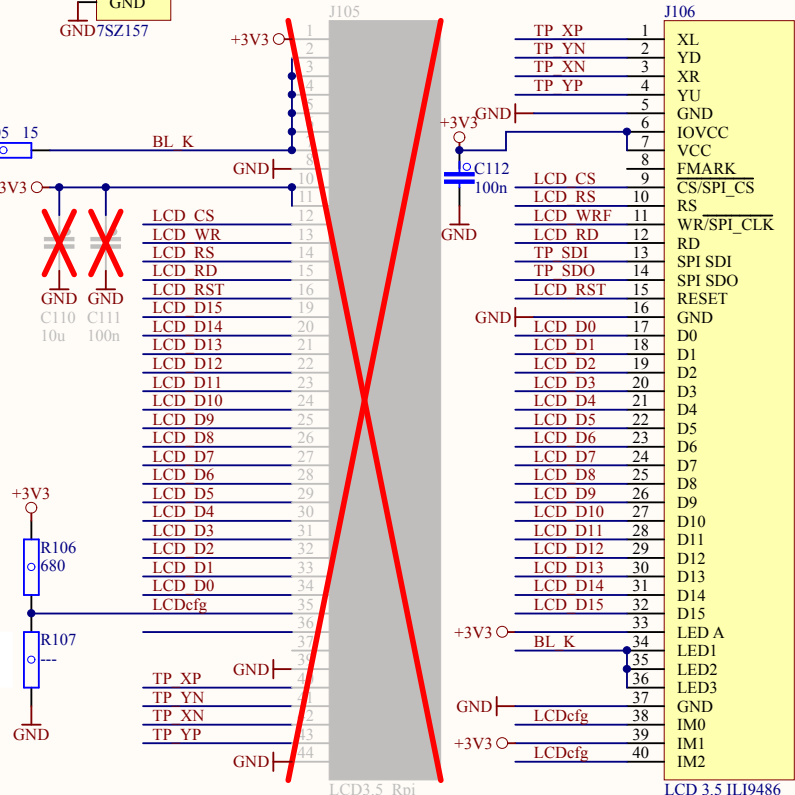


Title		
OVI40/mcHF lcd adapter		
Size	Number	Revision
A4		1.2
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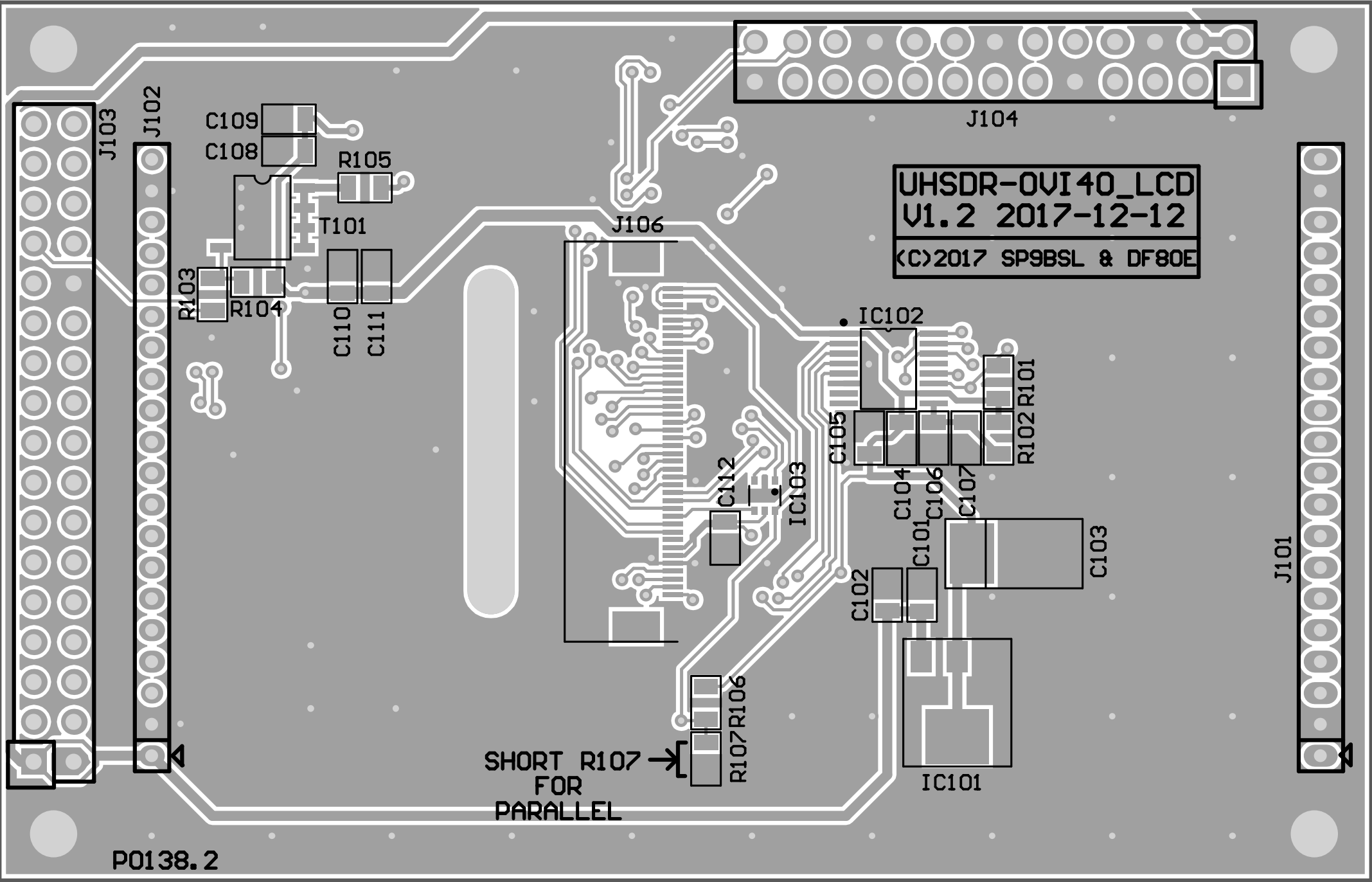
UI board connectors



Populate R107 with 0 ohm for parallel operation



Title			OVI40/mchF lcd adapter	
Size	Number		Revision	
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UHSDR-OVI40_LCD
V1.2 2017-12-12
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SHORT R107
FOR
PARALLEL

P0138.2

Raspberry PI 3.5" LCD
(SPI mode)

1

44PIN 3.5" LCD
(PARALLEL mode)

44